

FIG. 1 is a block diagram of a system for processing motion vectors and frames. The system includes an INPUTBUFFER 100, an MPEG DECODER 110, an MPEG ENCODER 150, an OUTPUTBUFFER 160, RAM1 (Frames) 120, and RAM2 (Motion Vectors) 140. The INPUTBUFFER 100 feeds into the MPEG DECODER 110. The MPEG DECODER 110 feeds into RAM1 (Frames) 120 and RAM2 (Motion Vectors) 140. RAM1 (Frames) 120 feeds into the MPEG ENCODER 150. RAM2 (Motion Vectors) 140 feeds into the MPEG ENCODER 150. The MPEG ENCODER 150 feeds into the OUTPUTBUFFER 160. A CONTROL signal is sent from the MPEG DECODER 110 to the MPEG ENCODER 150.

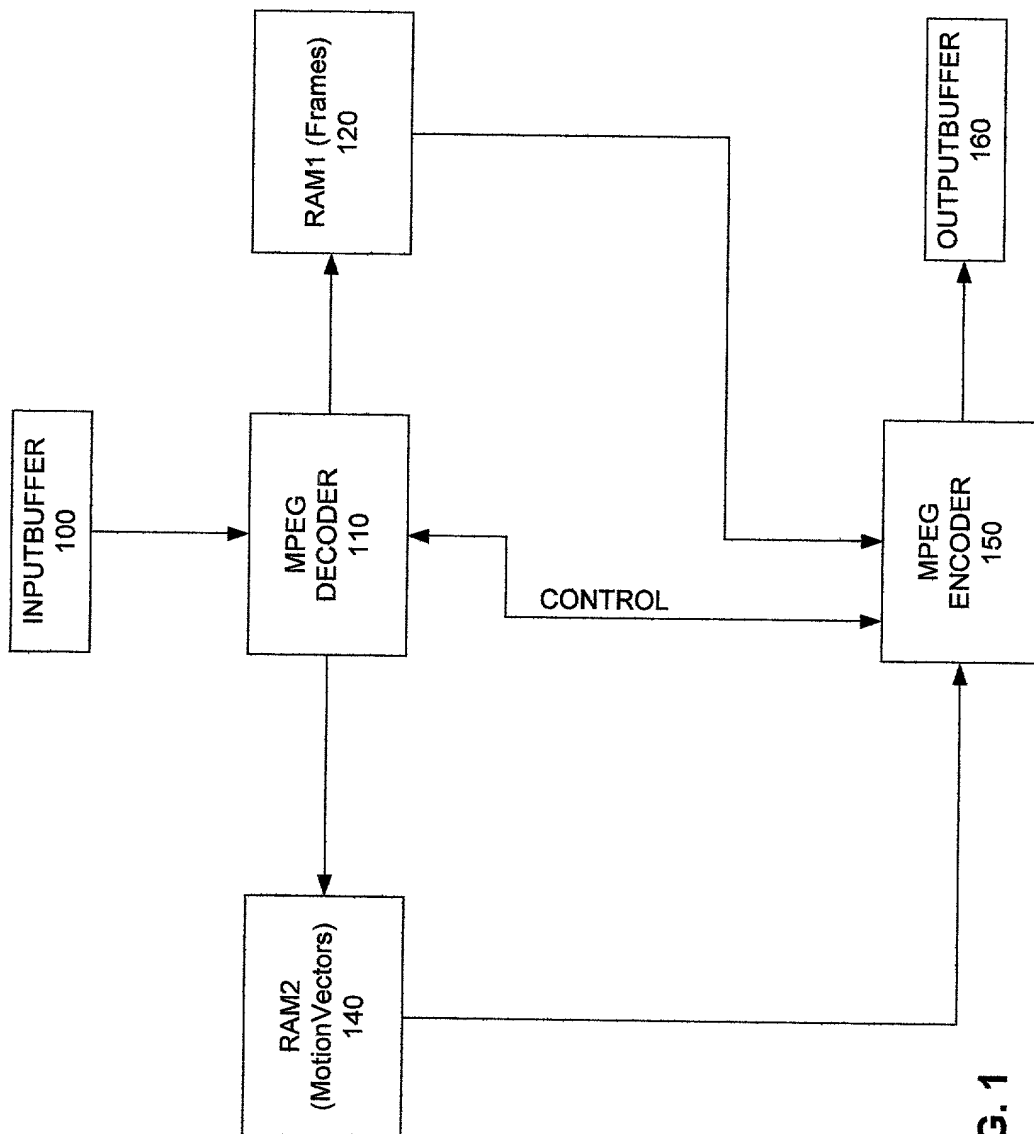


FIG. 1

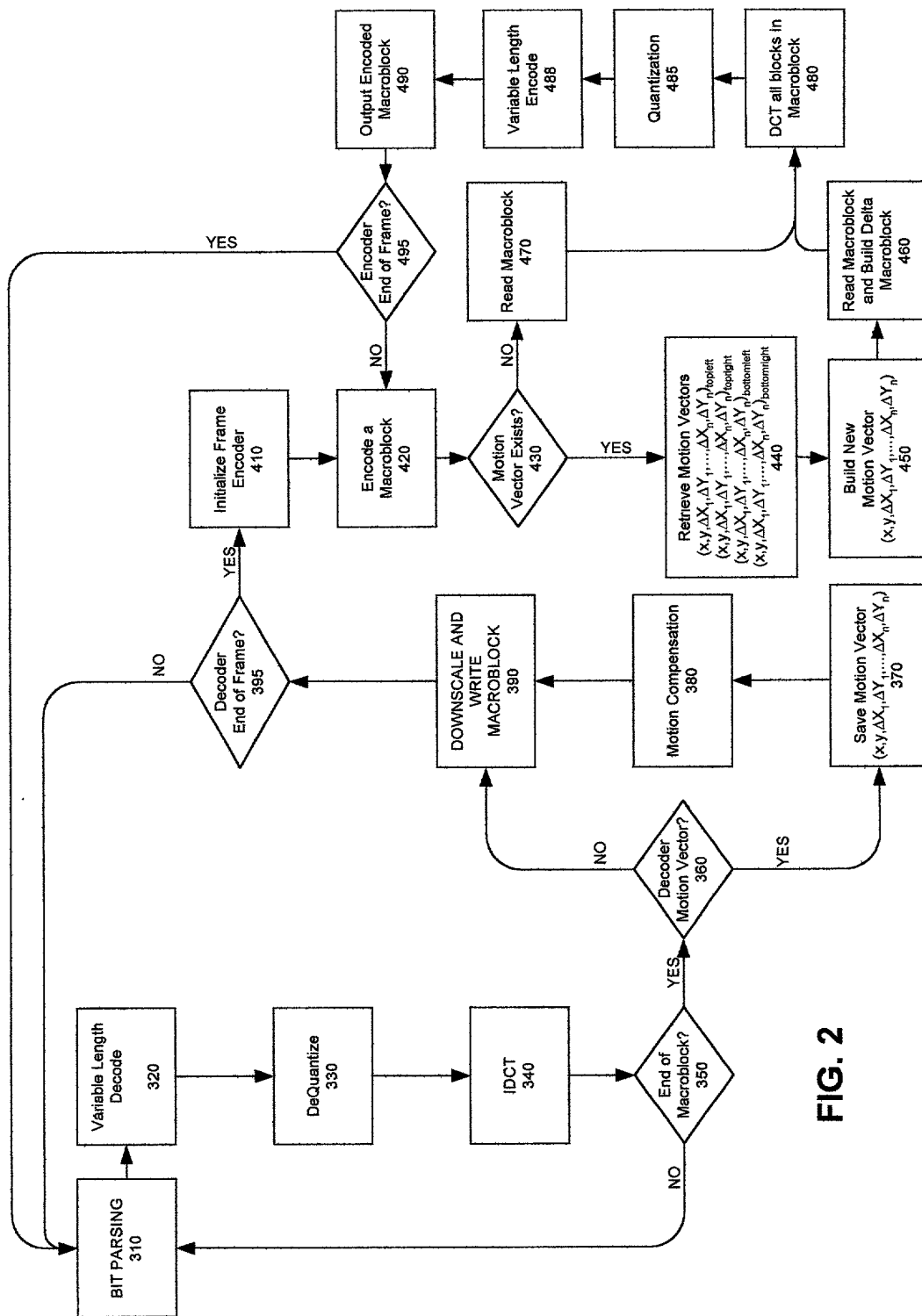
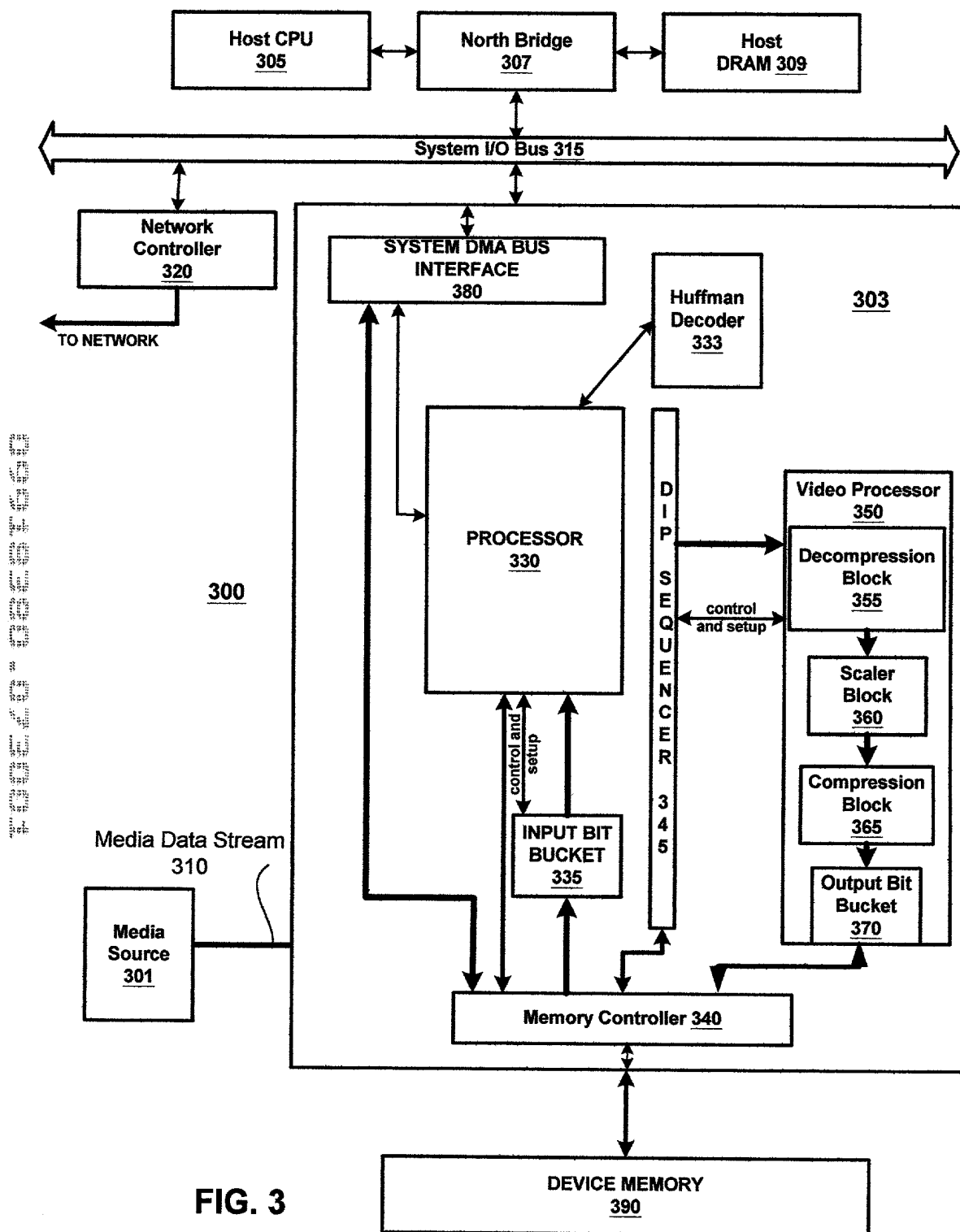
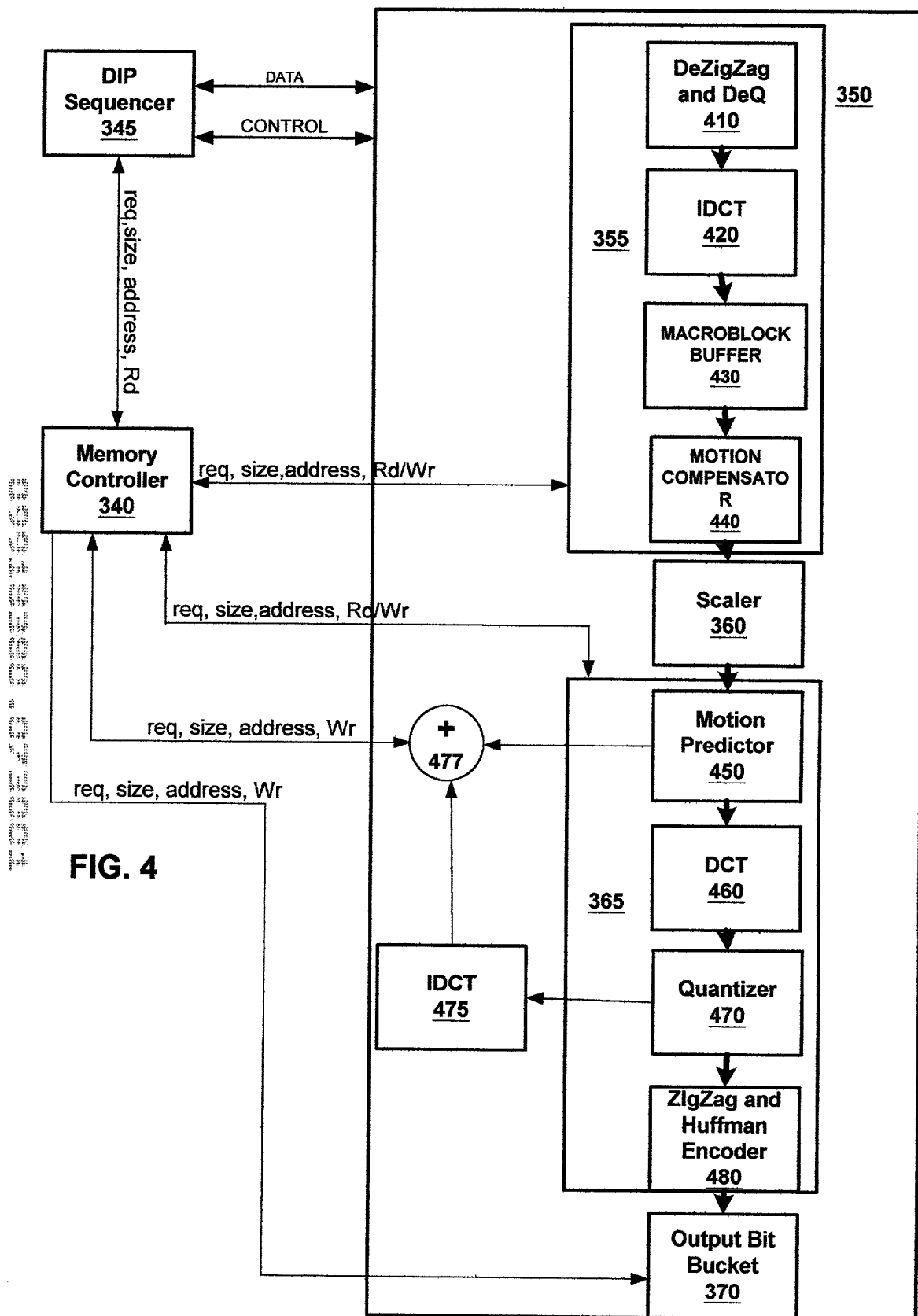


FIG. 2





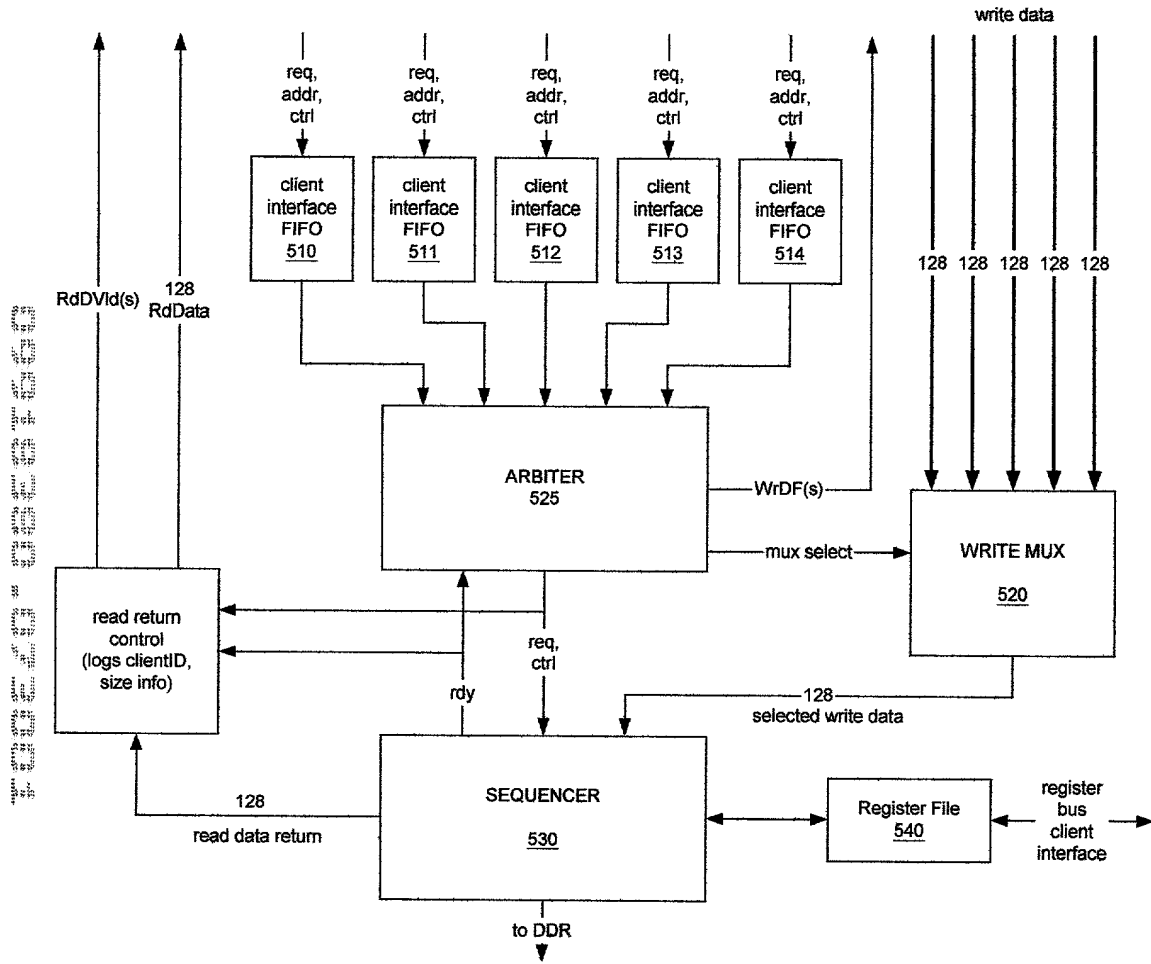


FIG. 5

600

FIG. 6

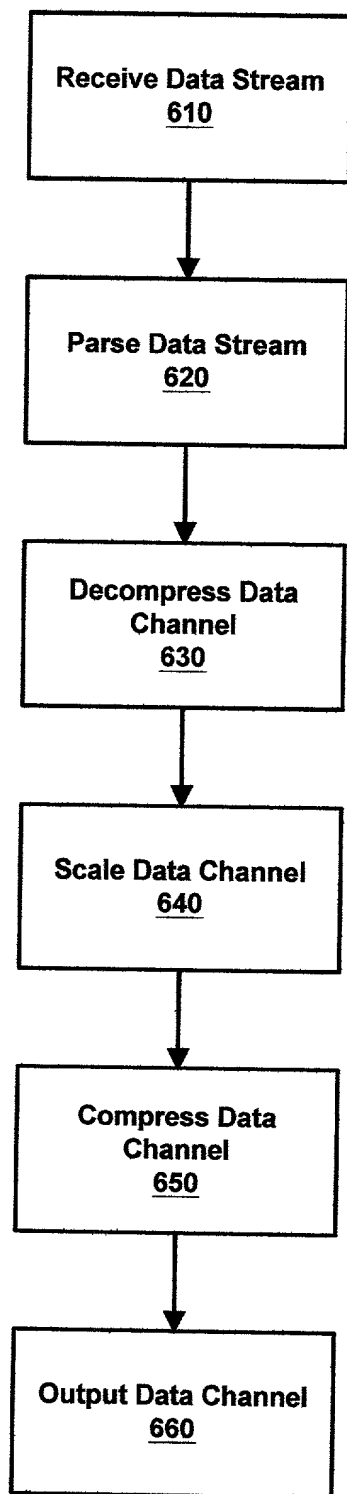


FIG. 7 is a block diagram of a data structure 700. The data structure 700 is divided into three main sections: a header section 710, a configuration section 720, and a data section 730. The header section 710 is further divided into an opcode field 711 and a size field 712. The configuration section 720 is connected to the header section 710 by a dashed line. The data section 730 is connected to the configuration section 720 by a dashed line.

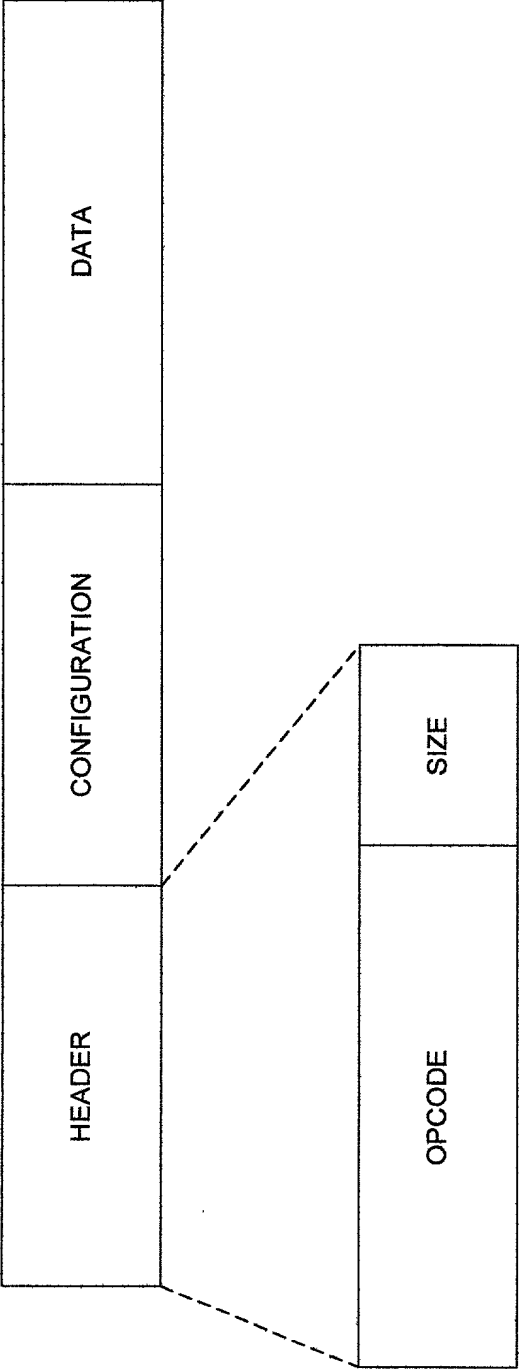


FIG. 7

FIG. 8 is a block diagram of a system 710 for processing data. The system 710 includes a data instruction packet generator 711, a pointer control 713, an elementary stream reader 712, elementary stream data 712, DIP data 392, a DIP pointer 393, and a data input controller 715. The data instruction packet generator 711 is connected to the pointer control 713. The pointer control 713 is connected to the elementary stream reader 712 and the DIP pointer 393. The elementary stream reader 712 is connected to the elementary stream data 712. The elementary stream data 712 is connected to the DIP data 392. The DIP data 392 is connected to the DIP pointer 393. The DIP data 392 and the DIP pointer 393 are connected to the data input controller 715. The data input controller 715 is connected to a controller 716.

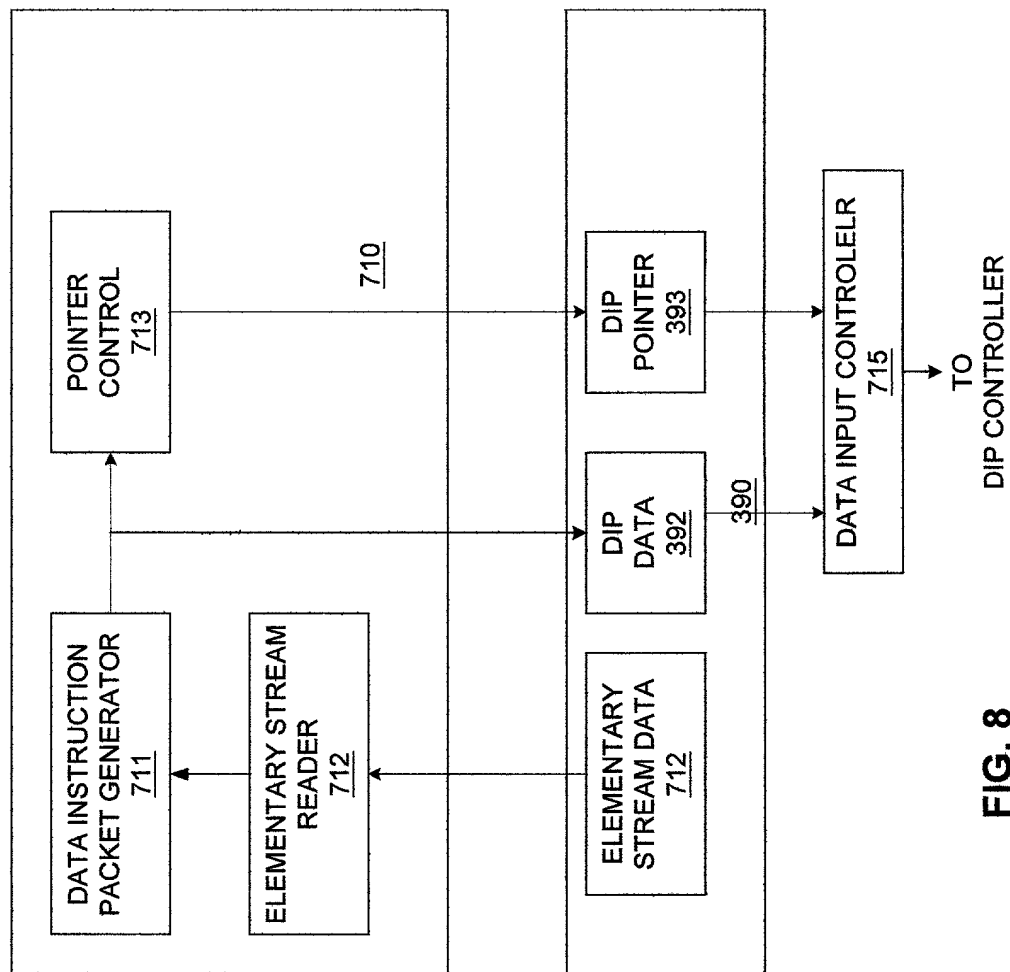


FIG. 9

[illegible]

FIG. 10

D(0,0)
D(1,0)
D(2,0)
D(3,0)
.
.
.
D(n,m)

FIG. 11

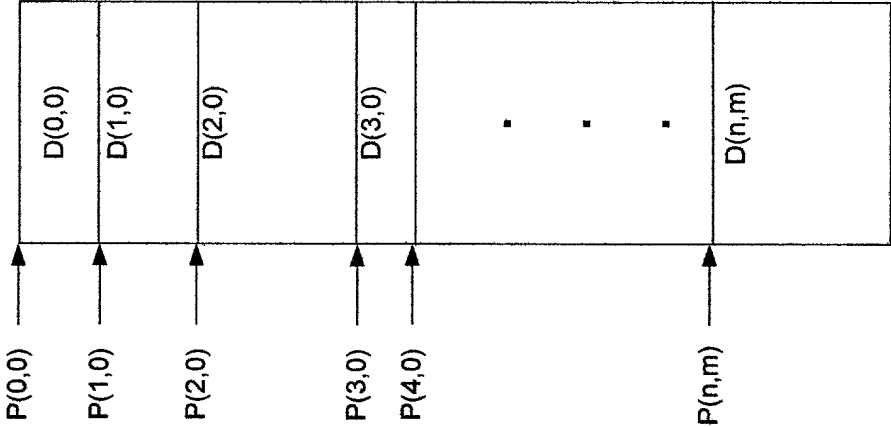


FIG. 13

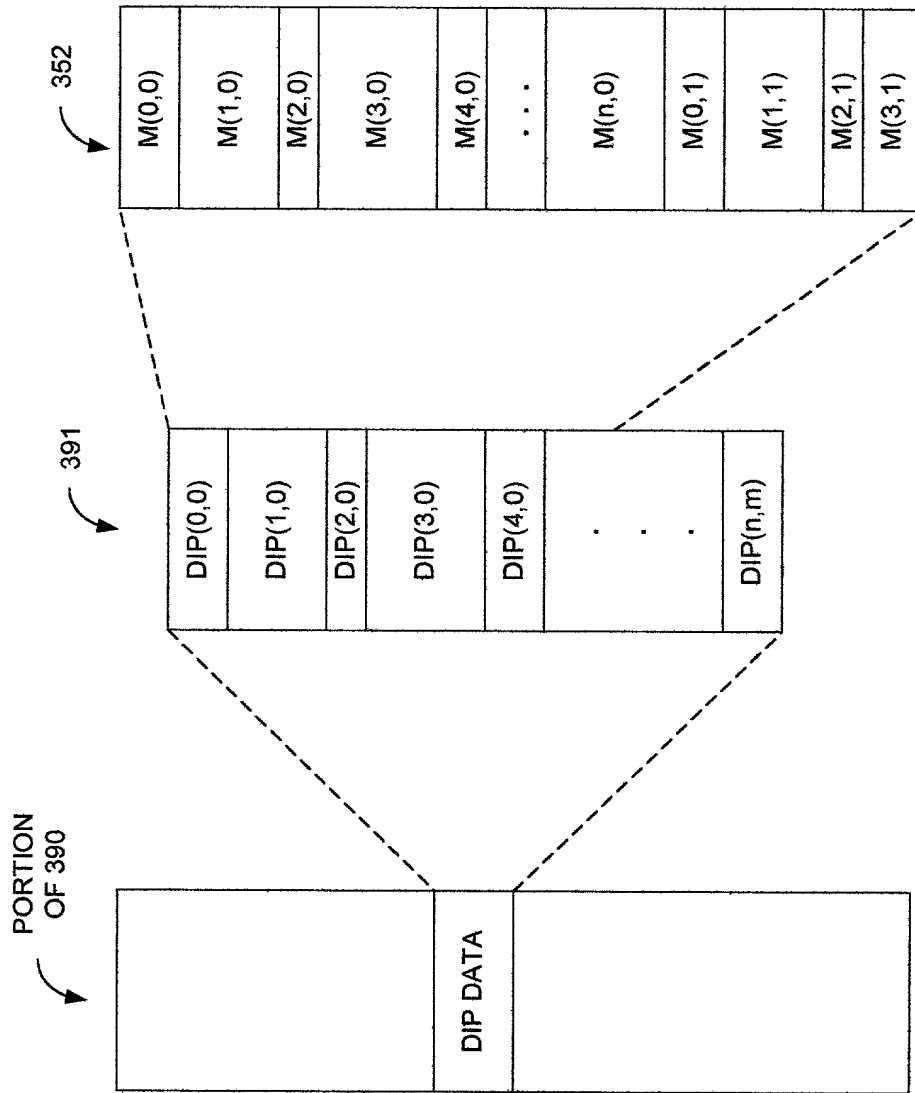


FIG. 12

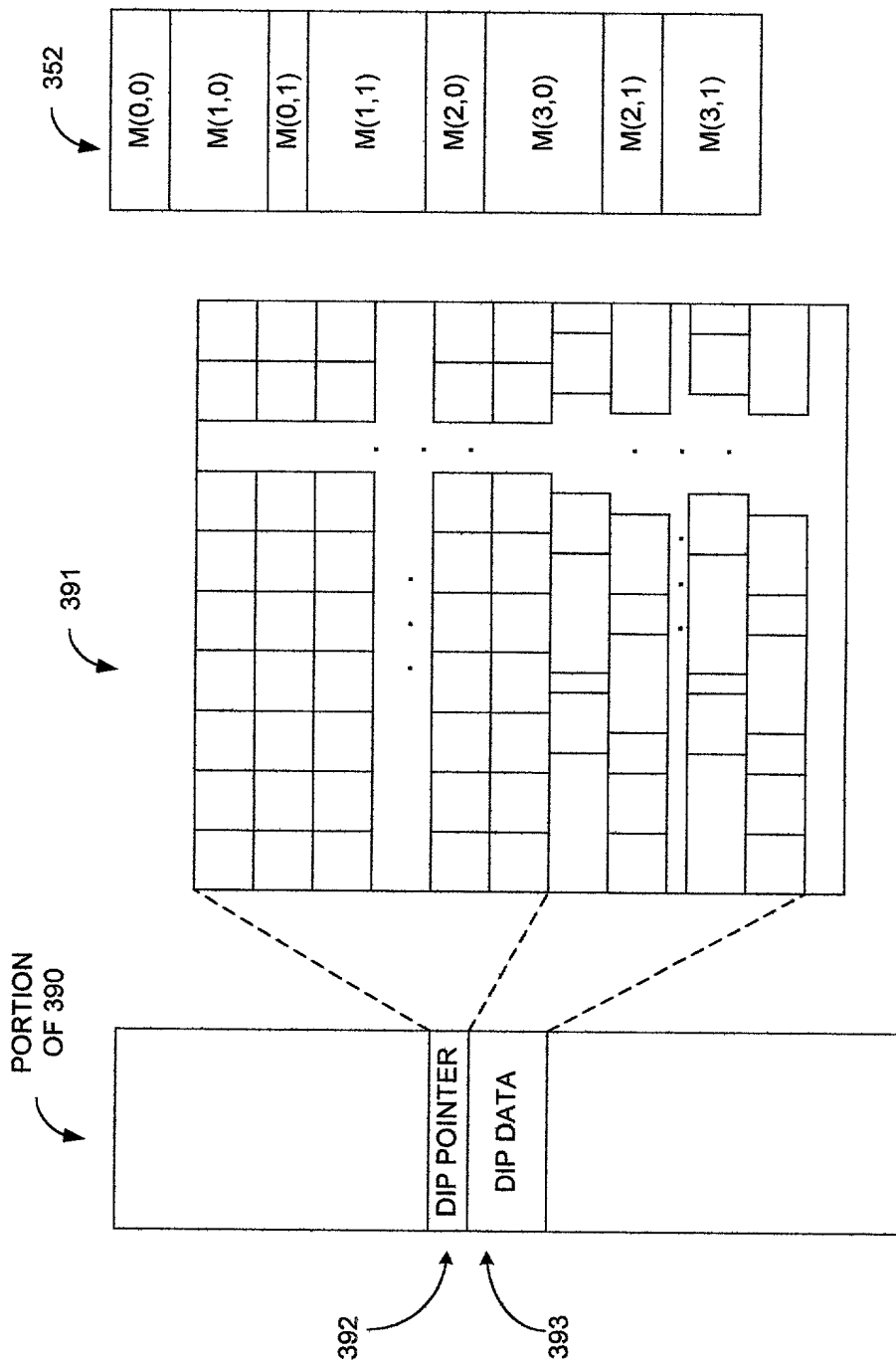


FIG. 14

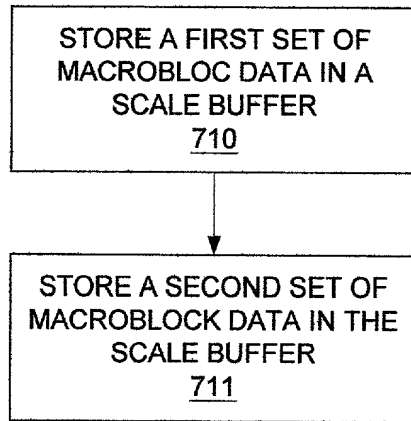


FIG. 15

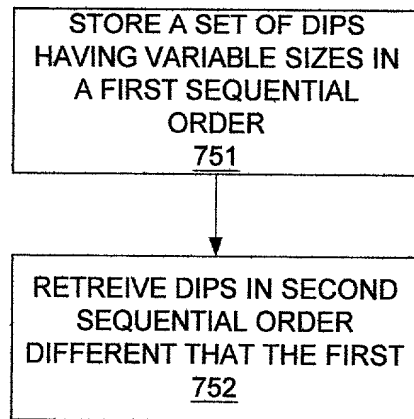


FIG. 19

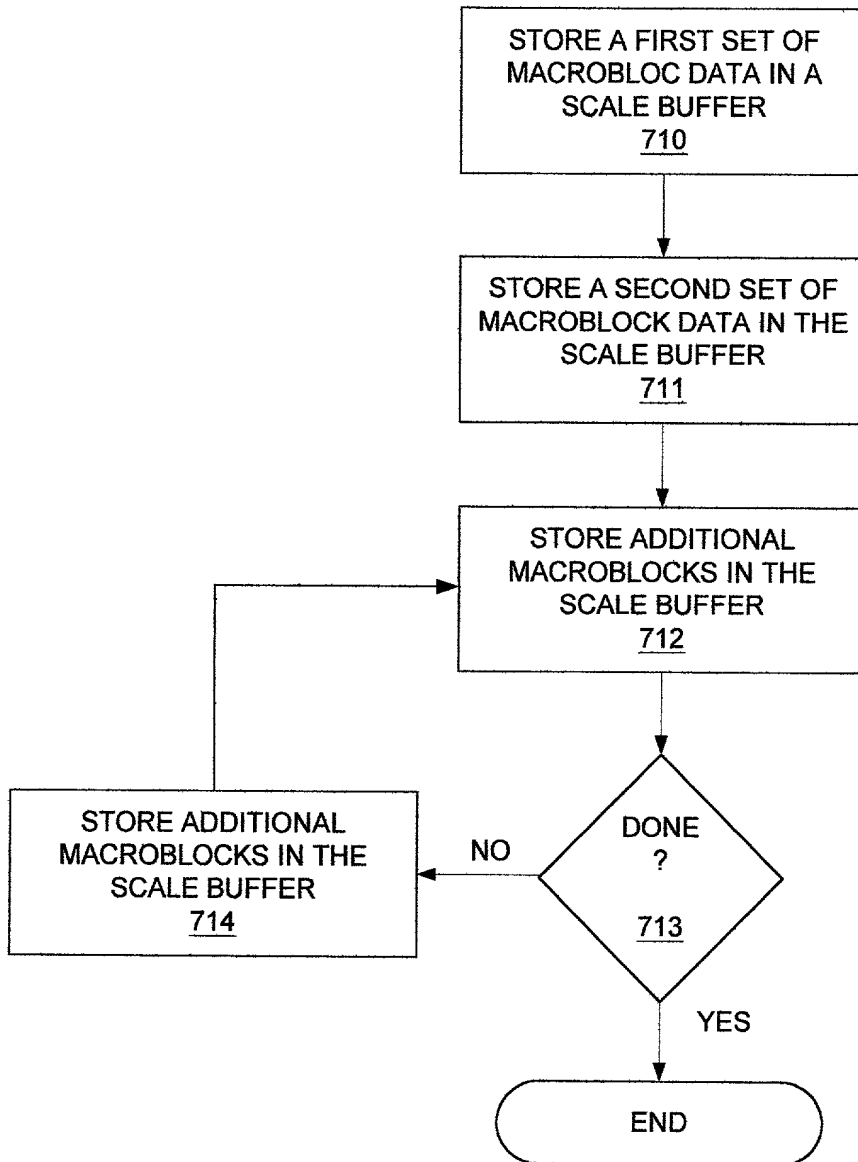


FIG. 16

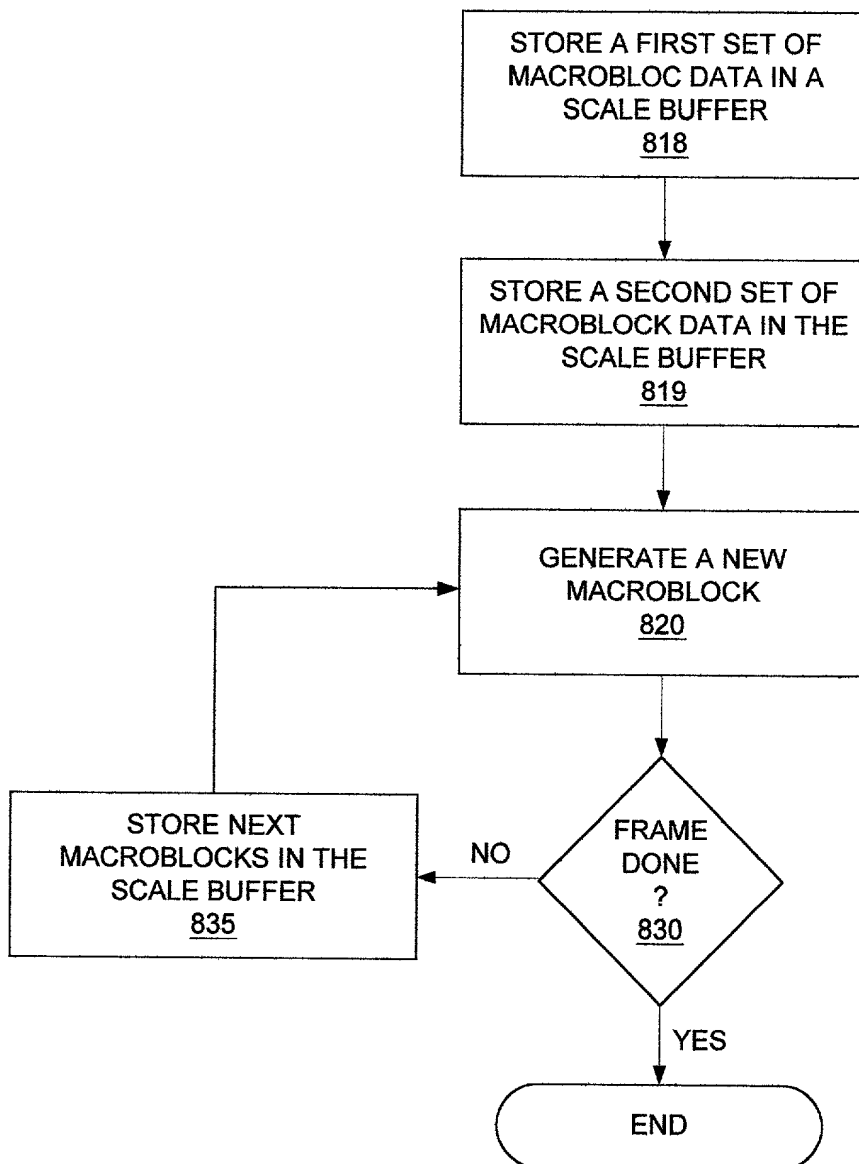


FIG. 17

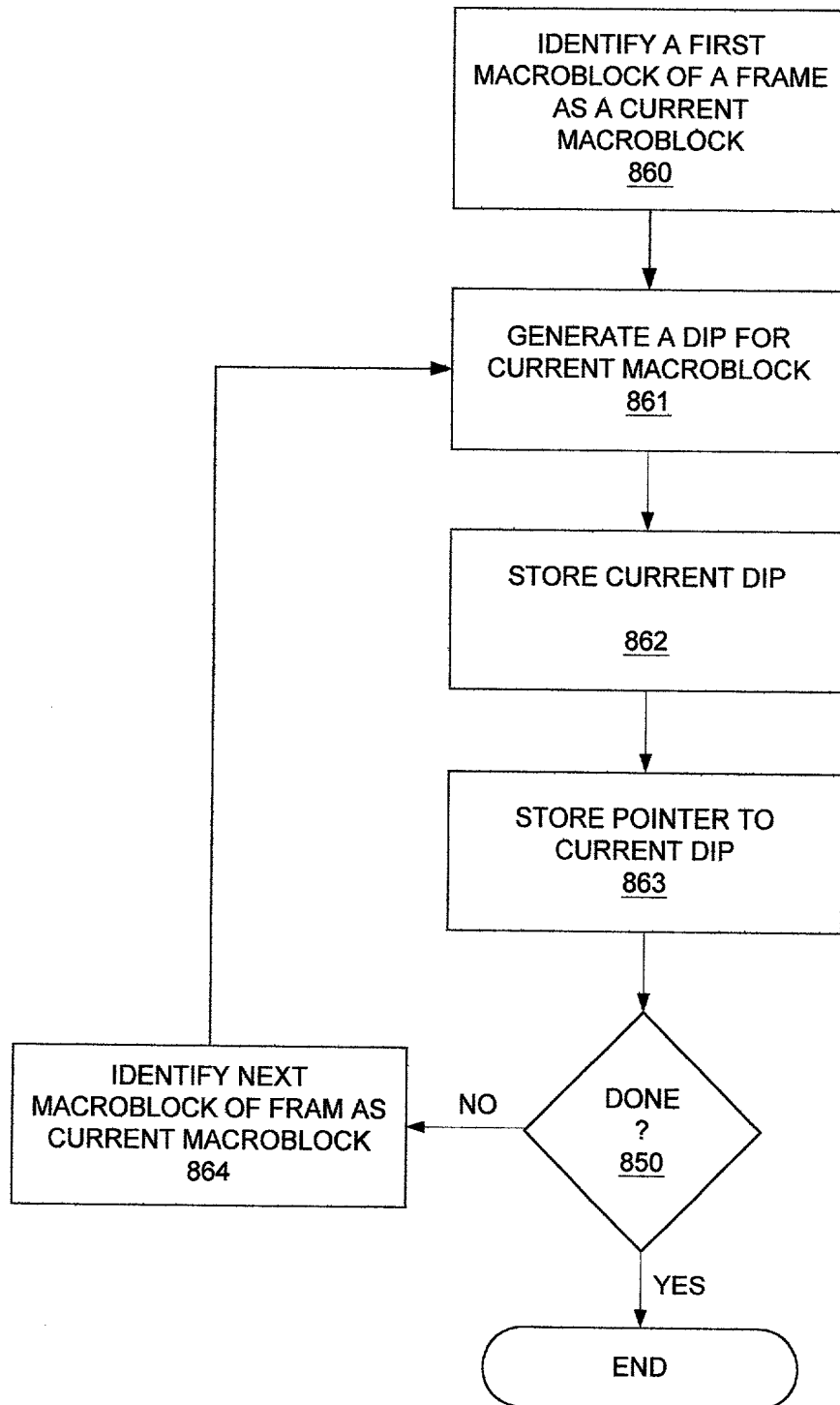


FIG. 18

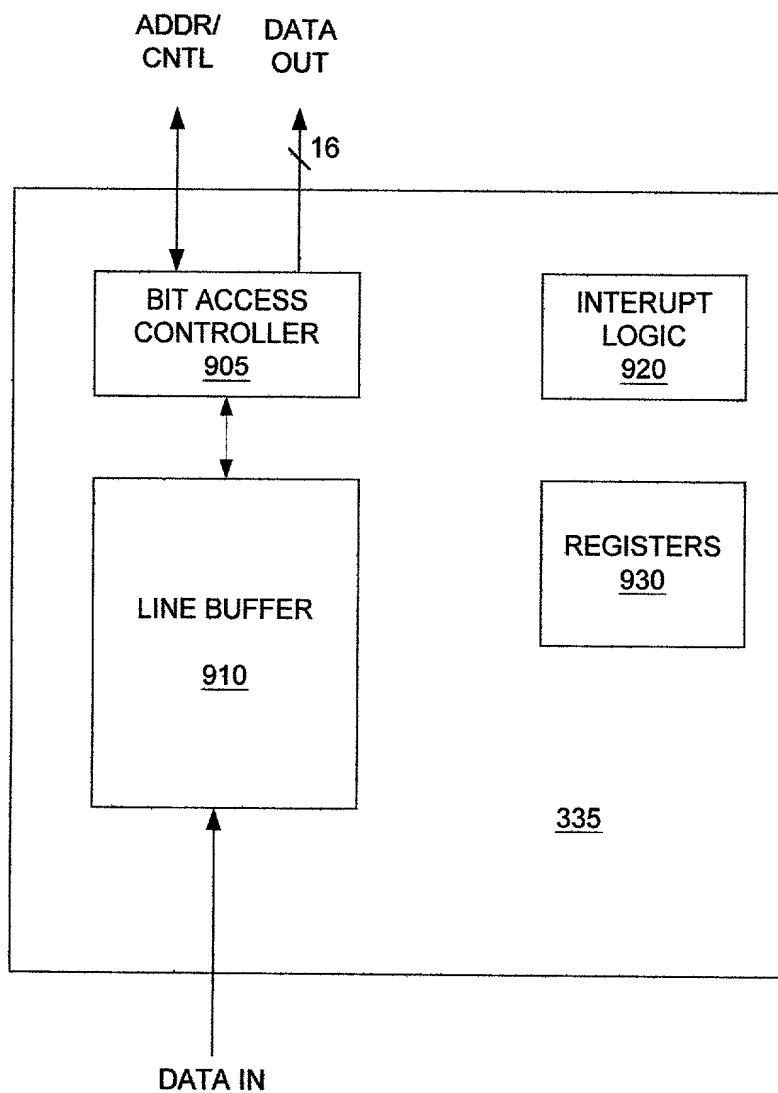


FIG. 20

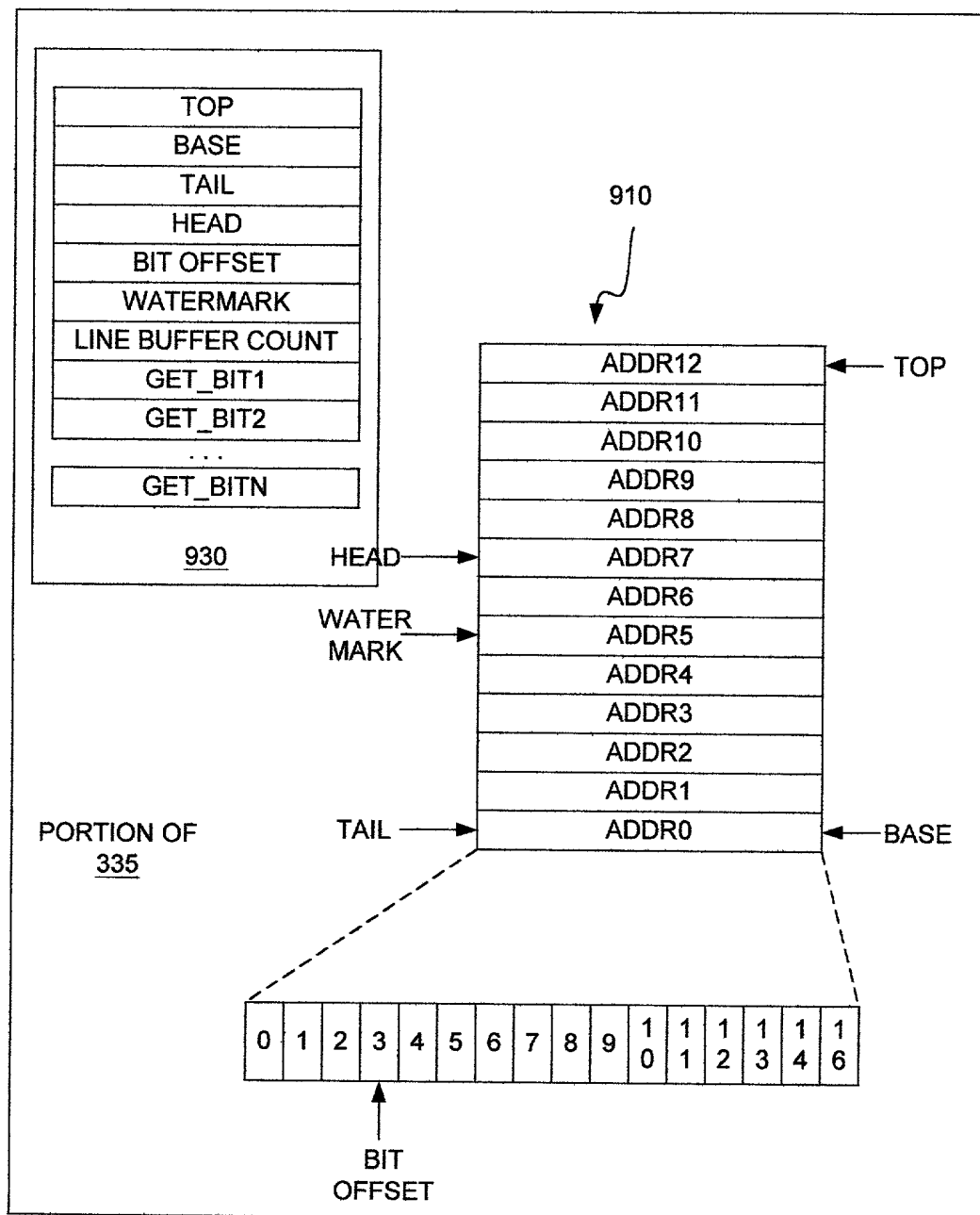


FIG. 21

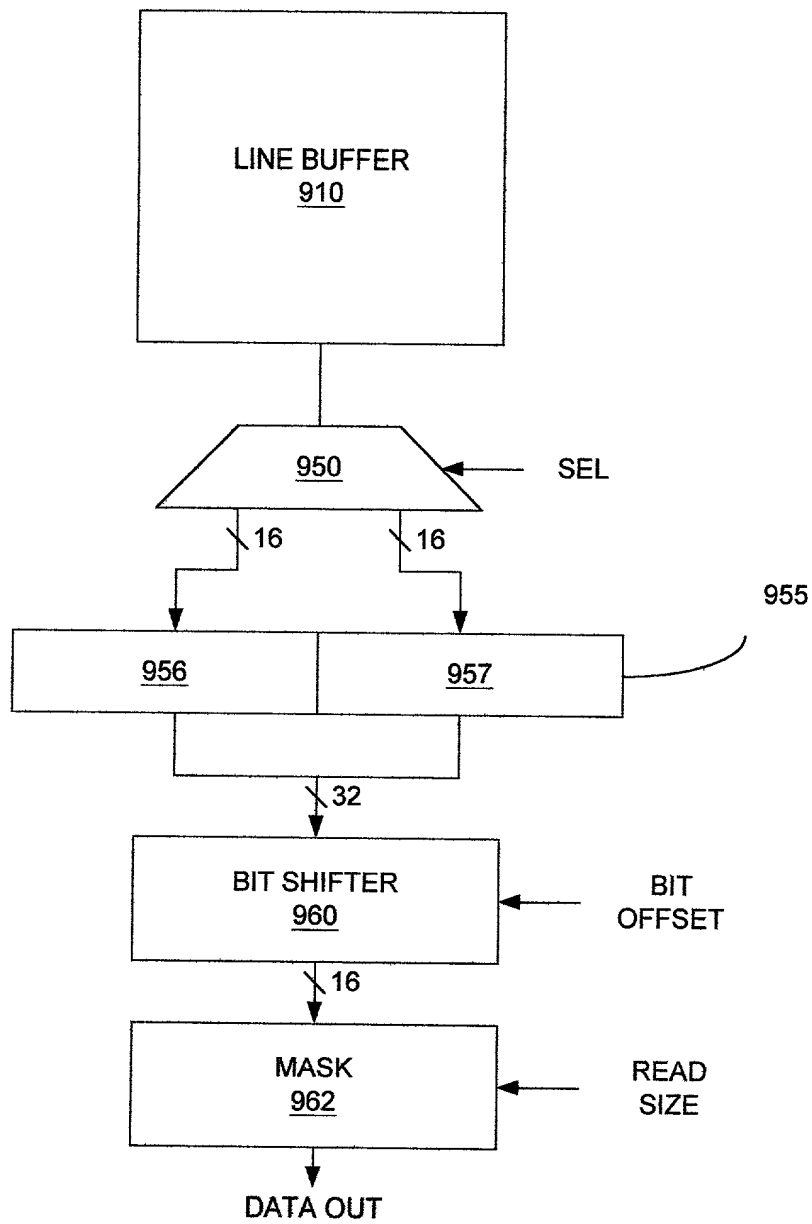


FIG. 22

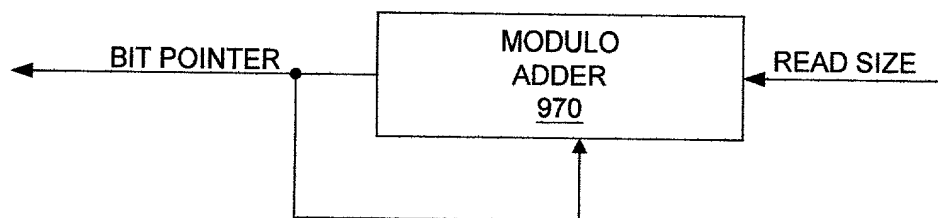


FIG. 23